



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of: Brown, et al. )  
Filed: January 28, 2002 ) Confirmation No.: 3628  
Serial No.: 10/058,863 ) Group Art Unit: 2836  
Title: Real-Time Digital Voltage ) Examiner: Deberadinis, Robert L.  
              Sag Compensator )  
Atty Docket No.: ATA-5 )

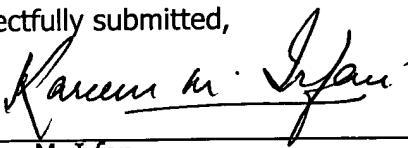
**SUBMISSION OF FORMAL DRAWINGS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith are five sheets of formal drawings (Figs. 1-5) to be substituted for the informal drawings presently on file in the above-identified application. The Official Draftsperson is respectfully requested to approve these drawings for entry into the application.

Respectfully submitted,

  
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**CERTIFICATE UNDER 37 CFR 1.8**

I hereby certify that this document is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to: Commissioner of Patents, POB 1450, Alexandria, VA 22313-1450, Mail Stop Issue Fee, on this 14th day of August, 2006.

  
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Laura G. Weiss